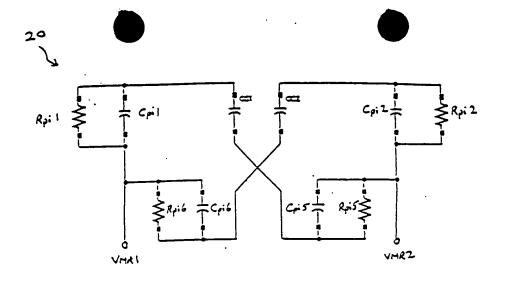
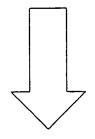


FIG. 1



Equivalent Input Circuit



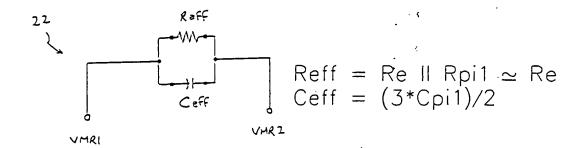


FIG. 25

FIG. 24

Simplified Circuit

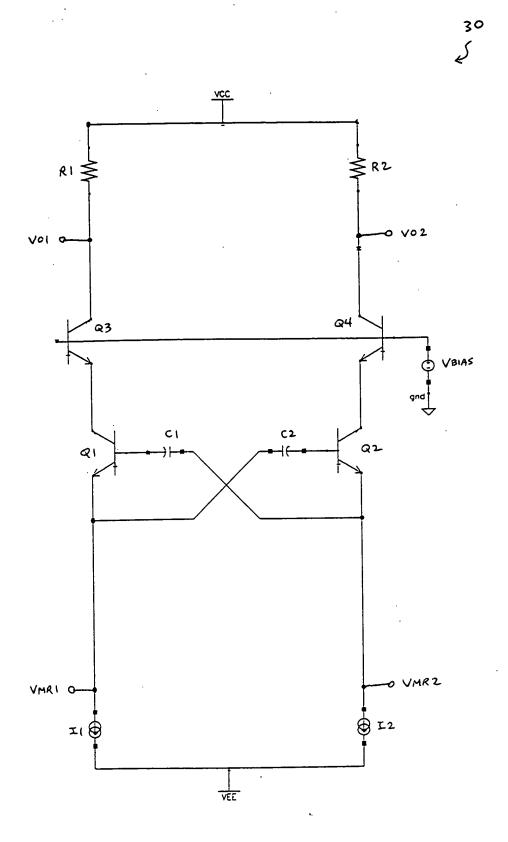


FIG. 3

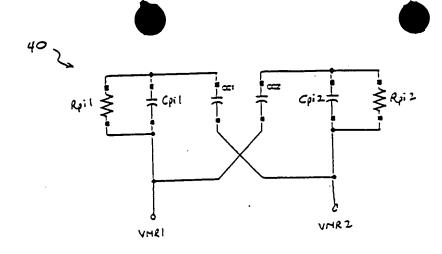
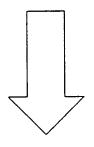
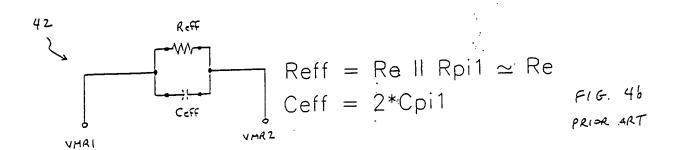


FIG. 40 PRIOR ART

Equivalent Input Circuit





Simplified Circuit

